

**AMENDMENTS TO THE CLAIMS:**

Please cancel claims 15, 17, 22, and 24, without prejudice or disclaimer of the subject matter thereof, and amend claims 11 and 18 as follows.

This listing of claims will replace all prior versions and listings of claims in the application:

1. - 10. (Canceled)

11. (Currently Amended) An antiferroelectric liquid crystal panel, having an antiferroelectric liquid crystal between a pair of substrates, which comprises a driving circuit adapted to output:

a layer structure controlling voltage waveform having a frequency of 1 Hz to 100 Hz and a voltage in the range of +10 V to +50 V or -10 V to -50 V, which changes a layer structure of the antiferroelectric liquid crystal from a chevron structure to a bookshelf structure; and

a display driving voltage waveform for normal display,

wherein the driving circuit comprises switches which switch the output from the display driving voltage waveform to the layer structure controlling voltage waveform for an optional length of time, and wherein

the switches are switched at predetermined intervals of time, or are switched in accordance with information from a temperature sensor provided with the liquid crystal panel.

12. (Previously Presented) An antiferroelectric liquid crystal panel, as claimed in claim 11, wherein said optional length of time is equal to the overall period of one frame.

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com

13. (Previously Presented) An antiferroelectric liquid crystal panel, as claimed in claim 11, wherein said optional length of time is equal to the period of one frame excluding a reset period.

14. (Previously Presented) An antiferroelectric liquid crystal panel, as claimed in claim 11, wherein the pair of substrates are provided with scanning electrodes and signal electrodes, and wherein the panel comprises a control circuit which outputs the layer structure controlling voltage waveform to the scanning electrodes.

15. (Canceled)

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16. (Previously Presented) An antiferroelectric liquid crystal panel, as claimed in claim 11, comprising a control circuit which outputs the layer structure controlling voltage waveform, when the information from the temperature sensor indicates a temperature change that reduces the layer spacing in the antiferroelectric liquid crystal.

17. (Canceled)

18. (Previously Presented) A method of driving an antiferroelectric liquid crystal panel having an antiferroelectric liquid crystal between a pair of substrates, wherein a display driving voltage waveform for normal display is output, and

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FARABOW  
GARRETT &  
DUNNER LLP

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Washington, DC 20005  
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Fax 202.408.4400  
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the display driving voltage waveform is switched for an optional length of time to a layer structure controlling voltage waveform having a frequency of 1 Hz to 100 Hz and a voltage in the range of +10 V to +50 V or -10 V to -50 V, which changes a layer structure of the antiferroelectric liquid crystal from a chevron structure to a bookshelf structure,

wherein the display driving waveform is switched to the layer structure controlling voltage waveform at predetermined intervals of time, or is switched in accordance with information from a temperature sensor provided with the liquid crystal panel.

19. (Previously Presented) A method of driving an antiferroelectric liquid crystal panel, as claimed in claim 18, wherein said optional length of time is equal to the overall period of one frame.

C\ 20. (Previously Presented) A method of driving an antiferroelectric liquid crystal panel, as claimed in claim 18, wherein said optional length of time is equal to the period of one frame excluding a reset period.

21. (Previously Presented) A method of driving an antiferroelectric liquid crystal panel, as claimed in claim 18, wherein the pair of substrates are provided with scanning electrodes and signal electrodes, and wherein the layer structure controlling voltage waveform is output to the scanning electrodes

22. (Canceled)

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HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com

23. (Previously Presented) A method of driving an antiferroelectric liquid crystal panel, as claimed in claim 18, wherein the layer structure controlling voltage wave form is output, when information from the temperature sensor indicates a temperature change that reduces the layer spacing in the antiferroelectric liquid crystal.

24. (Canceled)

25. (Previously Presented) An antiferroelectric liquid crystal panel as claimed in claim 11, wherein said layer structure controlling voltage waveform is a square wave.

26. (Previously Presented) A method of driving an antiferroelectric liquid crystal panel as claimed in claim 18, wherein said layer structure controlling voltage waveform is a square wave.

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

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Fax 202.408.4400  
www.finnegan.com